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Submission Number:	10888		
Application Number:	PCT/IB2006/053194		
Date of Receipt:	10 September 2006		
Receiving Office:	International Bureau of the World Intellectual Property Organization		
Your Reference:	IISC		
Applicant:	DHARMASENA, Suminda Sirinath Salpitikorala		
Number of Applicants:	1		
Title:	IMPLIED INSTRUCTION SET COMPUTING (IISC) / DUAL INSTRUCTION SET COMPUTING (DISC) / SINGLE INSTRUCTION SET COMPUTING (SISC) / RECURRING MULTIPLE INSTRUCTION SET COMPUTING (RMISC) BASED COMPUTING MACHINE / APPARATUS / PROCESSOR		
Documents Submitted:	IISC-appb.xml	142887	11 September 2006 00:53:48
	IISC-fees.xml	1820	11 September 2006 00:53:48
	IISC-vlog.xml	2843	11 September 2006 00:53:46
	IISC-othd-000002.pdf	231797	10 September 2006 22:41:22
	IISC-pdoc-000001.pdf	188572	10 September 2006 23:37:30
	IISC-othd-000001.pdf	280187	11 July 2006 22:29:18
	IISC-requ.xml	3401	11 September 2006 00:57:04
	PCT101.GML	2417	10 September 2006 21:05:37
	IISC-orig.zip	186300	11 September 2006 00:35:42
	IISC-decl.xml	2336	11 September 2006 00:53:48
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